

L Number	Hits	Search Text	DB	Time stamp
4	1		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/17 09:20
7	21	((sampl\$3) with ((activit\$3 traffic) near3 level\$3) with (function\$5 operat\$3) and (reserv\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/17 07:56
5	2	((sampl\$3 monitor\$3 track\$3) with ((activit\$3 traffic)) with (function\$5 operat\$3) same ((reconfigur\$7 re-configur\$7 reallocat\$3 re-allocat\$3) with resource)) and reserv\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/17 07:57
6	20	((sampl\$3 monitor\$3 track\$3) same ((activit\$3 traffic)) same (function\$5 operat\$3) same ((reconfigur\$7 re-configur\$7 reallocat\$3 re-allocat\$3) with resource)) and (reserv\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/17 08:17
-	78	(subset near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:36
-	0	((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((monitor\$3 near3 (activit\$3 function\$1 task\$1)) same (detect\$3 with (decreas\$3 lower\$3 reduc\$3)))) and ((subset near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:25
-	717	((subset portion) with (function task)) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:26
-	0	((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((monitor\$3 near3 (activit\$3 function\$1 task\$1)) same (detect\$3 with (decreas\$3 lower\$3 reduc\$3)))) and (((subset portion) with (function task)) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/24 07:54
-	990	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:35
-	314	((subset portion partition fraction) near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:36
-	214	((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2)) and (((subset portion partition fraction) near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:36
-	8	((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2)) and (((subset portion partition fraction) near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)))) and (bitstream)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 09:24

-	20	(((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2)) and (((subset portion partition fraction) near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)))) and (bitstream bit-stream (bit adj2 stream)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:37
-	159	(((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2)) and (((subset portion partition fraction) near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)))) and (resource near3 (task function program application)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:38
-	13	(((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2)) and (((subset portion partition fraction) near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)))) and (bitstream bit-stream (bit adj2 stream))) and (((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2)) and (((subset portion partition fraction) near4 resource) and ((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)))) and (resource near3 (task function program application)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:38
-	661	((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:47
-	236	((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) and ((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:48
-	31	((reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) and ((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA))) not configur\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 13:48
-	5	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and (allocat\$3 near3 resource\$1)) and ((monitor\$3 near3 (activit\$3 function\$1 task\$1)) same (detect\$3 with (decreas\$3 lower\$3 reduc\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 14:53
-	70	((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocate\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((detect\$3 monitor\$3) with level)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 15:35
-	227	((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocate\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and (detect\$3 monitor\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 14:57
-	31	((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocate\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and (load\$3 near3 balanc\$3)) and (((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocate\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((detect\$3 monitor\$3) with level))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/21 15:37
-	267	((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocate\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 14:56

-	201	((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((first second another third) near5 (task process\$3 activit\$3 function)) and (high\$3 increas\$3 low\$3 reduc\$3 decreas\$3 rais\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 16:03
-	92	(((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((first second another third) near5 (task process\$3 activit\$3 function)) and (high\$3 increas\$3 low\$3 reduc\$3 decreas\$3 rais\$3)) and (period\$6 time sampl\$3)) and (period\$6 and time and sampl\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 16:10
-	70	((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and (load\$3 near3 balanc\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 15:18
-	64	(((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((first second another third) near5 (task process\$3 activit\$3 function)) and (high\$3 increas\$3 low\$3 reduc\$3 decreas\$3 rais\$3)) and (((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and (load\$3 near3 balanc\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 16:09
-	35	((((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((first second another third) near5 (task process\$3 activit\$3 function)) and (high\$3 increas\$3 low\$3 reduc\$3 decreas\$3 rais\$3)) and (period\$6 time sampl\$3)) and (period\$6 and time and sampl\$3)) and (((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and (load\$3 near3 balanc\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 15:19
-	180	(((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((first second another third) near3 (task process\$3 activit\$3 function)) and (high\$3 increas\$3 low\$3 reduc\$3 decreas\$3 rais\$3)) and (period\$6 time sampl\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 16:08
-	64	(((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and ((first second another third) near3 (task process\$3 activit\$3 function)) and (high\$3 increas\$3 low\$3 reduc\$3 decreas\$3 rais\$3)) and (((((allocat\$3 near3 resource\$1) and (PLD pla pga FPGA DFPGA)) and (((recalculat\$3 reallocat\$3 reconfigur\$3 configur\$3 modif\$4 chang\$3 alter\$4) with resourc\$2) (dynamic\$4 with resource with alloca\$3))) and (load\$3 near3 balanc\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/22 16:10
-	278	(PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and ((allocat\$3 reallocat\$3 re-allocat\$3) near3 (ratio proportion\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/23 17:02
-	10	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1) and ((allocat\$3 reallocat\$3 re-allocat\$3) near3 (ratio proportion\$3))) and (resource near3 (reconfigur\$7 configur\$7))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/23 17:02

-	665	((PLD pla pga FPGA DFPGA cpld) and (allocat\$3 near3 resource\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/23 17:00
-	4	((PLD pla pga FPGA DFPGA cpld) and (allocat\$3 near3 resource\$1)) and ((allocat\$3 reallocat\$3 re-allocat\$3) near3 (ratio proportion\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/23 17:04
-	158	((PLD pla pga FPGA DFPGA cpld) and (allocat\$3 near3 resource\$1)) and (resource near3 (reconfigur\$7 configur\$7))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/24 07:56
-	14	((((PLD pla pga FPGA DFPGA cpld) and (allocat\$3 near3 resource\$1)) and (resource near3 (reconfigur\$7 configur\$7))) and ((reconfigur\$7 re-configur\$7 configur\$7) near3 (bitstream (bit adj2 stream))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/24 08:29
-	43	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with (activit\$3 level\$3 traffic))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 11:49
-	31	((((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with (activit\$3 level\$3 traffic))) and (((function\$5 task\$3 process processes processing) with (activit\$3 level\$3 traffic)) same (bus\$3 noise low\$3 high\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 09:33
-	156	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with (activit\$3 level\$3 traffic increas\$3 more less\$3 decreas\$3 low\$3 high\$3 demand\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 09:38
-	137	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with (increas\$3 more less\$3 decreas\$3 low\$3 high\$3 demand\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 09:39
-	117	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with (increas\$3 more less\$3 decreas\$3 demand\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 09:39
-	26	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with (increas\$3 decreas\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 12:10
-	0	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with ((activit\$3 adj2 level\$3) traffic) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 11:58
-	4	((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with (allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4)) same ((function\$5 task\$3 process processes processing) with ((activit\$3 adj2 level\$3) traffic) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 12:00

-	807	(reduc\$3 low\$3 high\$3 increas\$3 decreas\$3 less\$3 more) with ((allocat\$3 re-allocat\$3 de-allocat\$3) with resourc\$2) with (amount level\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 12:14
-	175	((function\$5) with ((activit\$3 adj2 level\$3)) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 13:16
-	0	((PLD\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with (allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4)) and (((function\$5) with ((activit\$3 adj2 level\$3)) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 13:17
-	2	(PLD\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) and (((function\$5) with ((activit\$3 adj2 level\$3)) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 13:17
-	15	((PLD\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with (allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4)) and (((PLD\$1 pla\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with ((allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4) with resourc\$2)) same ((function\$5 task\$3 process processes processing) with (increas\$3 decreas\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 13:13
-	325	((function\$5 operat\$3) with ((activit\$3 adj2 level\$3)) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 13:18
-	2	(PLD\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) and (((function\$5 operat\$3) with ((activit\$3 adj2 level\$3)) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 13:19
-	54	((PLD\$1 pga\$1 FPGA\$1 DFPGA\$1 cpld\$1) with (allocat\$3 re-allocat\$3 de-allocat\$3 reconfigur\$7 configur\$7 modif\$7 chang\$3 alter\$4)) and (((function\$5 operat\$3) with ((activit\$3) with (more less\$3 decreas\$3 increas\$3 reduc\$3 greater)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 13:21
-	336	(sampl\$3 monitor\$3 track\$3) with ((activit\$3 traffic) near\$3 level\$3) with (function\$5 operat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 16:03
-	0	((sampl\$3 monitor\$3 track\$3) with ((activit\$3 traffic) near\$3 level\$3) with (function\$5 operat\$3) same ((reconfigur\$7 re-configur\$7 reallocat\$3 re-allocat\$3) with resource))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 15:48
-	0	((sampl\$3 monitor\$3 track\$3) same ((activit\$3 traffic) near\$3 level\$3) same (function\$5 operat\$3) same ((reconfigur\$7 re-configur\$7 reallocat\$3 re-allocat\$3) with resource))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 15:47
-	40	((sampl\$3 monitor\$3 track\$3) same ((activit\$3 traffic)) same (function\$5 operat\$3) same ((reconfigur\$7 re-configur\$7 reallocat\$3 re-allocat\$3) with resource))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/17 07:56

-	11	((sampl\$3 monitor\$3 track\$3) with ((activit\$3 traffic)) with (function\$5 operat\$3) same ((reconfigur\$7 re-configur\$7 reallocat\$3 re-allocat\$3) with resource))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/17 07:56
-	305	(sampl\$3 monitor\$3) with ((activit\$3 traffic) near3 level\$3) with (function\$5 operat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/11 16:03
-	94	(sampl\$3) with ((activit\$3 traffic) near3 level\$3) with (function\$5 operat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/17 07:56